



FORM PTO-1449

SUPPLEMENTAL
INFORMATION DISCLOSURE STATEMENTATTY. DOCKET NO.
1778.1730000APPLICATION NO.
09/702,112INVENTORS
Jensen et al.FILING DATE
October 30, 2000ART UNIT
2183

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOC. REF.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
RUE	AA1	0 109 567 A2	05/1984	EP0	—	—	N/A
RUE	AB1	0 169 565 A2	01/1986	EP0	RECEIVED	—	N/A
RUE	AC1	0 199 173 A2	10/1986	EP0	SEP 17 2004	—	N/A
RUE	AD1	0 264 215 A2	04/1988	EP0	Technology Center 2100	—	N/A
RUE	AE1	0 272 198 A2	06/1988	EP0	—	—	N/A
RUE	AF1	0 324 308 A2	07/1989	EP0	—	—	N/A
RUE	AG1	WO 95/30187 A1	11/1995	WO WIPO	—	—	N/A

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

RUE	AH1	Bandyopadhyay, A. and Zheng, Y.F., "Combining Both Micro-Code And Hardwired Control In RISC," 5 pages, (published in ACM SIGARCH Computer Architecture News, Vol. 15, Issue 4, pp. 11-15 (September 1987)).
RUE	AI1	Bursky, D., "Software-Efficient RISC Core Trims System-Memory Needs," Reprinted from <i>Electronic Design</i> , Penton Publishing, Inc., 3 pages (March 20, 1995).
RUE	AJ1	Hayashi, T. et al., "A 5.6-MIPS Call-Handling Processor for Switching Systems," <i>IEEE Journal of Solid-State Circuits</i> , Vol. 24, No. 4, IEEE, pp. 945-950 (August 1989).
RUE	AK1	"High Performance Dual Architecture Processor," <i>IBM Technical Disclosure Bulletin</i> , Vol. 36, No. 2, IBM Corp., pp. 231-234 (February 1993).
RUE	AL1	<i>Intel486™ Microprocessor Family Programmer's Reference Manual</i> , Intel Corporation, 1992, pp. 23-5, 23-9, and 23-6.
RUE	AM1	McNeley, K.J. and Milutinovic, V.M., "Emulating a Complex Instruction Set Computer with a Reduced Instruction Set Computer," <i>IEEE Micro</i> , IEEE, pp. 60-72 (February 1987).
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EXAMINER

Richard Ellis

DATE CONSIDERED

11/8/2004

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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RLE	AA2	WO 95/30188 A1	11/1995	WO WIPO	—	—	N/A
RLE	AB2	WO 96/24895 A1	08/1996	WO WIPO	—	—	N/A
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	AG2						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AH2	
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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18
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Title of Invention	Boundary Address Registers for Selection of ISA Mode																																																																																
<div>Application Number: 09/702112</div> <div>Confirmation Number: 8333</div> <div>First Named Applicant: Michael JENSEN</div> <div>Attorney Docket Number: 1778.1730000</div> <div>Art Unit: 2183</div> <div>Examiner: Richard L. Ellis</div> <div>Search string: (3543245 or 3631405 or 3949372 or 4077058 or 4084235 or 4274138 or 4285040 or 4456954 or 4575797 or 4727480 or 4774652 or 4782443 or 4835734 or 4839797 or 4868740 or 4876639 or 4992934 or 5091846 or 5193158 or 5255379 or 5280593 or 5307504 or 5327566 or 5335331 or 5339422 or 5420992 or 5430862 or 5438668 or 5438672 or 5481684 or 5481693 or 5519873 or 5542059 or 5574887 or 5574927 or 5574928 or 5598546 or 5619665 or 5638525 or 5652852 or 5685009 or 5740461 or 5774686 or 5781750 or 5796973 or 5828859 or 5867682 or 5905893 or 6012138 or 5867681).pn.</div> <div>RECEIVED SEP 17 2004 Technology Center 2100</div>																																																																																	
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RE	10	4727480	1988-02-23	Albright et al.
	11	4774652	1988-09-27	Dhuey et al.
	12	4782443	1988-11-01	Matsumoto
	13	4835734	1989-05-30	Kodaira et al.
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RUE	47	5867682	1999-02-01	Witt et al.
RUE	48	5905893	1999-05-18	Worrell
RUE	49	6012138	2000-01-04	Worrell
RUE	50	5867681	1999-02-02	Worrell et al.

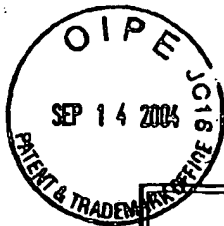
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